INVENTOR INFORMATION

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CORRESPONDENCE INFORMATION

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APPLICATION INFORMATION

Title Line One:: BIT ERROR RATE TESTER IMPLEMENTED IN A P

Title Line Two:: ROGRAMMABLE LOGIC DEVICE

Total Drawing Sheets:: 4
Formal Drawings?:: Yes
Application Type:: Utility
Docket Number:: 174/297

Secrecy Order in Parent Appl.?:: No

REPRESENTATIVE INFORMATION

Representative Customer Number:: 36981

Registration Number One:: 39774
Registration Number Two:: 26183
Registration Number Three:: 31069
Registration Number Four:: 47943

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